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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	
MORIN ET AL.)	
)	
Serial No. 10/701,165)	Examiner: S. GEBREMARIAM
)	
Confirmation No. 5541)	Art Unit: 2811
)	
Filing Date: November 4, 2003)	
)	
For: SEMICONDUCTOR DEVICE WITH MOS)	
TRANSISTORS WITH AN ETCH-STOP LAYER)	
HAVING AN IMPROVED RESIDUAL STRESS)	
LEVEL AND METHOD FOR FABRICATING)	
SUCH A SEMICONDUCTOR DEVICE)	

AMENDMENT AFTER FINAL

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Official Action of August 24,
2005, please enter the amendments and consider the remarks set
out below.

12/27/2005 TL0111 00000011 10701165
01 FC:1251 120.00 OP